IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (currently amended): A processing apparatus, comprising:

a processing apparatus body for executing a prescribed process to a target object;

a control mechanism for controlling said processing apparatus body; and

an information storage section for receiving a signal inputted and outputted to and from said control mechanism containing information necessary for grasping an operational record of said processing apparatus body during a real-time execution of said prescribed process to said target object, and for storing said information every ~~prescribed time period~~ 2 seconds,

wherein said information includes at least alarm data including data notifying malfunctions and troubles in said processing apparatus body during the execution of said prescribed process.

Claim 2 (original): The processing apparatus according to claim 1, wherein:

said control mechanism comprises a first and second controller each of which executes different control of said processing apparatus body; and said information storage section inputs at least one signal among a signal from said first controller, a signal from said second controller, and a signal transmitted and received between said first controller and said second controller.

Claims 3-8 (canceled)

Claim 9 (currently amended): An information storage apparatus for storing information in a processing apparatus including a processing apparatus body for executing a prescribed process to a target object and a control mechanism for controlling said processing apparatus body, said information storage apparatus comprising:

an information storage section for storing information; and

signal supply means for inputting a signal inputted and outputted to and from said control mechanism containing information necessary for grasping an operational record of said processing apparatus body during a real-time execution of said prescribed process to said target object, and supplying said information storage section with said signal, said information storage section storing said information included in said signal every ~~prescribed time period~~ 2 seconds,

wherein said information includes at least alarm data including data notifying malfunctions and troubles in the processing apparatus body during the execution of said prescribed process.

Claim 10 (original): The information storage apparatus according to claim 9, wherein said control mechanism comprises:

a first and second controller each of which executes different control of said processing apparatus body, wherein said information storage section inputs at least one signal among a signal from said first controller, a signal from said second controller, and a signal transmitted and received between said first controller and said second controller.

Claims 11-15 (canceled)

Claim 16 (currently amended): An information storage method for storing information in a processing apparatus including a processing apparatus body for executing a prescribed process to a target object and a control mechanism for controlling said processing apparatus body, comprising the steps of:

inputting a signal containing information necessary for grasping an operational record of said processing apparatus body during a real-time execution of said prescribed process to said target object from said control mechanism; and

storing said information included in said signal every ~~prescribed time period~~ 2 seconds,

wherein said information includes at least alarm data including data notifying malfunctions and troubles in said processing apparatus body during the execution of said prescribed process.

Claim 17 (original): The information storage method according to claim 16, wherein: said control mechanism comprises: a first and second controller each of which executes different control of said apparatus body; and said information is at least one information included in said signal among a signal from said first controller, a signal from said second controller, and a signal transmitted and received between said first controller and said second controller.

Claims 18-21 (canceled)

Claim 22 (previously presented): The information storage method according to claim 16, wherein said signal is inputted for each processing executed for each target object, or each lot of said target objects.

Claims 23-24 (canceled)

Claim 25 (currently amended): A processing system, comprising:

a plurality of treatment processing apparatus bodies each of which executes prescribed process to a target object;

a plurality of control mechanisms each of which controls each of said plurality of treatment processing apparatus bodies;

a data storage section for receiving signals inputted and outputted to and from said plurality of control mechanisms containing information necessary for grasping an operational record of said treatment processing apparatus [[body]] bodies during a real-time execution of said prescribed process to said target object, and for storing said information every prescribed time period;

an information process section for receiving said information from said plurality of treatment processing apparatus bodies and analyzing said information[[,]]; and

a monitor computer connected through a communication network with said information process section,

wherein said information includes at least alarm data including data notifying malfunctions and troubles in said treatment processing apparatus [[body]] bodies during the execution of said prescribed process, and

wherein said monitor computer receives through said communication network at least one information among: information for controlling said plurality of treatment processing apparatus bodies; measurement information; alarm information; operation information in said plurality of treatment processing apparatus bodies; transport information of said target object; and sensor information from sensors which belong to said plurality of treatment processing apparatus bodies and are connected only with said information storage section.

Claims 26-27 (canceled)

Claim 28 (currently amended): The processing system according to claim [[27]] 25, wherein said monitor computer is connected with a display for displaying at least one information among: said measurement information; said alarm information; said operation information in said plurality of processing apparatuses; said transport information of said target object.

Claim 29 (previously presented): The processing system according to claim 25, wherein said control mechanism comprises a display for displaying at least one information among recipe information, maintenance information, measurement information, operation information in each of said plurality of processing apparatuses; and transport information of said target object.

Claim 30 (previously presented): The processing apparatus according to claim 1, further comprising:

detection means for detecting at a prescribed timing an information quantity stored in said information storage section; and

an information erase mechanism for erasing prescribed information stored in said information storage section, when a still available memory capacity of said information storage section is smaller than a prescribed value, by comparing the detected information quantity with a quantity of information of a next processing.

Claim 31 (canceled)

Claim 32 (previously presented): The processing apparatus according to claim 30, wherein said erase mechanism erase said information quantity of a next processing in the same order that it was stored, when said still available memory capacity is smaller than said quantity of information of said next processing.

Claim 33 (canceled)

Claim 34 (currently amended): The ~~processing~~ information storage apparatus according to claim 9, further comprising:

detection means for detecting at a prescribed timing an information quantity stored in said information storage section; and

an information erase mechanism for erasing prescribed information stored in said information storage section, when a still available memory capacity of said information storage section is smaller than a prescribed value, by comparing the detected information quantity with a quantity of information quantity of a next processing.

Claim 35 (canceled)

Claim 36 (currently amended): The ~~processing~~ information storage apparatus according to claim 34, wherein said erase mechanism erase said information quantity of a next processing in the same order that it was stored, when said still available memory capacity is smaller than said quantity of information of said next processing.

Claim 37 (canceled)

Claim 38 (previously presented): The information storage method according to claim 16, further comprising:

detecting at a prescribed timing an information quantity stored in said information storage section; and

erasing prescribed information stored in said information storage section, when a still available memory capacity of said information storage section is smaller than a prescribed value, by comparing the detected information quantity with a quantity of information of a next processing.

Claim 39 (canceled)

Claim 40 (previously presented): The information storage method according to claim 38, wherein said information quantity of a next processing is erased when said still available memory capacity is smaller than said quantity of information of said next processing.

Claims 41-43 (canceled)